

Class	Subclass
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>150</i> Q.A. <i>Ed</i>	PATENT DATE
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APPLICATION NO. 09/955288	CONTR/PRIOR D F	CLASS 438	SUBCLASS 371	ART UNIT 2815	EXAMINER Jung
<p>APPLICANTS Joo-Hyong Lee</p> <p>TITLE Latch-up resistant CMOS structure</p>					

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
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